



Chairman's Message

Greetings to all Members of IEEE Hyderabad Section

With great pleasure I wish to congratulate all the members of IEEE Hyderabad section on receiving the 2018 IEEE R10 Outstanding Large Section Award. It is certainly a milestone for the section that we have won this award in 2011, 2015 and again repeated our success in 2018. This sustained and periodical laurels is a strong indication of the dedicated efforts of all members. I thank all the members for their contributions and expect that they will work with similar passion in future also.

I am pleased to see that the activities being organised by IEEE Hyderabad section are being supported by IEEE Headquarters and Region 10. More particularly the IEEE CIS/GRSS Society has been successful in getting grants of over USD 15,000 in the year 2018 for conduct of various programs and projects. The summer school on Computational Intelligence is the first such program being organised by the Hyderabad Section and I hope that other chapters will also follow up on this example.

I recommend that all society chapters apply for project funding from their respective chapter headquarters and bring in quality projects to the section. This will not only place the section on the international forefront of carrying out quality projects but also give a boost to the membership development efforts wherein new members can be inducted to be part of these challenging projects. I feel this channel of improving membership will be a very effective one, apart from the regular volunteer activities. This will keep volunteers more connected and result oriented.

The CASS/EDS Chapter is planning an IEEE Conference on Modeling of Systems Circuits and Devices (MOS AK India 2019) at IIT Hyderabad from 25-27 February 2018 and the call for papers is released in a pre conference event held recently. The technical society chapters and affinity groups are doing well and more efforts are required in reporting the events conducted through website, newsletter at the section, region and headquarter level. This will improve the visibility of the section. The coming months are packed with planned events by all chapters and I wish all the members a great experience ahead.

P.V.S. Maruti Rao

IEEE Hyderabad Section Mission and Vision

Vision

To be an essential resource for the professional development in specific fields of interest of IEEE for the benefit of mankind.

Mission

To provide a platform for knowledge sharing, networking, and career development to the local technical community.

**Hearty Congratulations to all members
IEEE Hyderabad Section wins the
2018 IEEE R10 Outstanding Large Section Award**

Achievements by Society Chapters and Individual Members

- 1) Computational Intelligence Society Chapter (CIS) received a grant of USD 8000 to conduct CIS Summer School in November 2018.
- 2) Geo Remote Sensing Society (GRSS) received a grant of USD 6000 under scheme of GRSS Student Grand Challenge for project "Development of surveillance system for forest fire detection and monitoring using Drones".
- 3) Geo Remote Sensing Society (GRSS) received a grant of USD 2400 for project "Development of Geo-spatial Platform for real time remote monitoring and forecasting of atmospheric constituents/pollutants using IoT and machine learning".
- 4) Dr. Sreelakshmi, Chair, IEEE WiE Affinity group was awarded with a grant of USD 6152 for conduct of workshops on innovative Systems, Man and Cybernetics (SMC) in Hyderabad section and all other subsections.
- 5) Ms. Meghna Reddy & Mr. Nagaraju from IEEE Computer society student chapter of IEEE Vaagdevi Engineering College Student Branch won the prestigious Richard E Merwin scholarship.
- 6) The IEEE WiE affinity group of IEEE MJCET Student Branch was awarded with a grant of USD 500 for organising a workshop on Women in Robotics under the R10 WiE Visibility Enhancement through Events Program. It is among the five proposal selected from across the Region 10.
- 7) K. Deergha Rao, Founder Chairman of IEEE COMSOC and SP chapter of Hyderabad Section published following two books:
 - K.Deergha Rao and MNS Swamy, "Digital Signal Processing Theory and Practice", Springer Nature, Singapore, 2018
 - K. Deergha Rao, "Signals & Systems", Springer Nature, Birkhauser, USA, 2018

Section Activities

Mini POCO 2018

IEEE Hyderabad Section and IEEE India Council has continued its leadership role in conference organizing by successfully conducting the Fourth Conference Organizers Workshop on 20th May 2018 with support from IEEE Region 10, IEEE Meetings, Conferences and Events and other IEEE Indian Sections. Over 70 participants joined from nearly every leading Indian city, representing many leading Academics institutes and Industries. This workshop was designed to serve the needs of the IEEE conference organizer through education, discussion, and access to experts. Emphasis was placed on some of the most important issues for conferences in the changing world, such as ethical practices, publication in IEEE Conferences and Journals and conference best practices.

MiniPOCO 2018 comprised of talks from experienced IEEE Conference Organizers and panel discussions with focused sessions. It gave ample opportunity for participants to exchange ideas, success stories and challenges with other trained and experienced IEEE Conference organizers. The workshop discussed important issues for organising conferences in the changing world. Issues like Conference timelines, Conference finances and ethical practices, Publications in IEEE Conferences and Journals, Conference best practices were discussed during one-day workshop at Vishakhapatnam.



Group photo with participants



Speaker delivering the talk



Participants discussion with speaker



Online Speaker's session

Resource persons for this workshop were Kevin Hanft and John Tracy from IEEE MCE USA, Lance Fung - Murdoch University, SN Singh - Vice Chancellor- MMMUT, Deepak Mathur - ONGC Ahmedabad, Anil Roy - DAICT Ahmedabad, Atul Negi - University of Hyderabad, Amit Kumar - BioAxis DNA Research Centre Hyderabad, Maruthi Pathapati - Vidcentum R&D Hyderabad, S Lakshminarayana - NSTL and Jacek Zurada - IEEE CIS. This event was also co-sponsored by Region 10 of IEEE and received appreciations from IEEE Region 10 and Conferences team in USA. Attendees felt satisfied participating in miniPOCO 2018 and marked positive feedbacks with a recommendation to organize such workshops for longer duration.

International Conference on Innovative Technologies in Engineering (ICITE 2018)

A Three-day International Conference on Innovative Technologies in Engineering was organized by four departments at University College of Engineering, Osmania University as a part of Osmania University Centenary Celebrations. This conference was technically co-sponsored by IEEE Hyderabad Section. B.V.R. Mohan Reddy, Chairman, Cyient was the chief guest of the inaugural function on 11th April 2018.



L to R: P.K.Shyamala, Ch. Gopal Reddy, B.V.R.Mohan Reddy, Sameen Fatima, G.Mallesham , P.V.S. Maruthi Rao

ICITE 2018 technical program featured various innovations and state-of-the-art technologies in the field of Biomedical Engineering, Computer Science and Engineering, Electrical and Electronics Engineering and Electronics and Communication Engineering. The Technical program committee received around 350 research papers from nine countries such as IRAN, Bangladesh, China, Yemen, Saudi Arabia and Srilanka. Each paper was reviewed by three reviewers and a total of 279 reviewers from various institutes and research labs extended their support to review the papers submitted to this conference.

Based on the recommendations of TPC, 115 papers are accepted for oral presentations across all streams and 21 papers are selected for poster presentations taking the acceptance ratio to 38.8%



B.V.R. Mohan Reddy lighting the lamp



P.V.S. Maruthi Rao being felicitated by organisers

The conference started by a tutorial session on 11th April 2018 on the areas of Machine Learning and Internet of Things. The inaugural session was scheduled on evening of 11th April 2018. The second day started with a keynote address by Prof. Ramgopal Rao, Director, IIT Delhi. It was followed by five technical sessions with four parallel tracks in each stream. The session chairs and co-chairs were from industry, research labs and academic institutes and evaluated the oral presentations. Two best papers from each stream were selected based on parameters like novelty, presentation and experimental results of the work and awards in all the four streams were presented at the closing ceremony of this conference on 13th April 2018.

Section Student Congress- SSC 2018

A two day “Section Student Congress - Flag ship event of IEEE Hyderabad Section” was held on 11th and 12th of August 2018 in DST Auditorium, University of Hyderabad. More than 300 participants including Professors, Branch Counselors, Chapter Advisors and Students members of various student branches of IEEE Hyderabad Section across the states of AP and Telangana attended the congress.

The day1 event begin with registration from 8:30 AM - 9:30 AM followed by IEEE videos and then Inauguration & Keynote Address by Chief guest **K .Narayana Murthy**, Professor and Dean, School of Computer and Information Sciences (SCIS), University of Hyderabad. This was followed by a technical talk by Toshio Fukuda (IEEE Japan Council) though WebX where he addressed the gathering about technologies of Intelligent systems and about his research on Modular Robotic Systems called Cellular Robotic Systems which are important for providing more natural movement in robots. The next session had P.V.S. Maruthi Rao (Chair, IEEE Hyderabad section) and N. Venkatesh (Secretary ,IEEE Hyderabad section) addressing the gathering on IoT and Edge Intelligence. Ms. Deepthi Ravula, CEO, Women Entrepreneurship Hub (WE HUB) gave a talk on “**Building an innovation ecosystem in the Government of Telangana.**” She gave awareness about opportunities for women provided by Government of Telangana in entrepreneurship.



A section of audience of SSC 2018

After the lunch break, a Talk by Mr. B. Nagesh, Life Skills Coach at Free Lancer, on the topic of “Engineering Students Psychology” was appreciated by gathering. This was followed by a Technical talk by Mr. Sourav Karmakar, Founder & Head, INFINOS TECH LLP on “Space Technology, Research and Education. At the same time a parallel meeting of all branch counsellors of section was conducted in AI lab of school of computer science. This gave an opportunity to all counsellors to interact with each other and section execom to discuss and resolve operational issues. A Technical talk by Dr. M. Lakshminarayana, Immediate past Chair, IEEE Hyderabad section on “Microwave Education – Opportunities and Challenges” was well received by the participants.

The second day of the event started at 09:00 AM with Best Student Practices by SAC and SSR. A technical talk was given by Dr. P.V. Raj Gopal, Chair, PES/IAS/PEL Joint Chapter, IEEE Hyderabad Section where he introduced about “New Product Development – Innovation, Technology Management & Market Development” and gave a brief idea about how to make a project into a product. The next technical talk was delivered on “Trends ,Challenges and oppurtunities in Microsystem-on-Chip Design” by Dr.Vijaya Sankara Rao Pasupureddi , Centre for Advanced studies in Electronics Science and Technology (CASEST), University of Hyderabad he also shared his ideas about how to design a chip and make it effective one and scope in Microsystem-on-Chip Design.

A professional talk on “Building Trust Through communication Skills” by Dr. Usha Bala Varanasi, Coordinator, WIE-Vizag Bay

Sub-Section where she spoke on how to develop communication skills and also how to overcome your weaknesses. A very motivating session on “Moulding Brain for Better Communication” with focus on how to develop inner peace in oneself was delivered by Dr. Sonali. She expressed her views on how meditation improves one’s mental and physical health.



Dr. Deepak Mathur being felicitated by the organising committee of SSC 2018

The next few Lectures were on “Innovation in Retail” by Mr . Satya Nagesh Nittala, Colruyt India and on “See Sound Live” by Dr. Krishna Nagarjan, CEO, CouthIT. In the afternoon session there was a talk on “New Opportunities with Industry 4.0” by Dr.D Ramakrishan Chair , Guntur Sub Section followed by a echnical talk by Bala Prasad Peddigri ,Chair –IEEE Computer Society ,Hyderabad Section in which he gave a brief explanation on “How Digital Innovation Driving Growth : Role of Artificial Intelligence Block Chain”. The technical session closed with a panel discussion on “Fuelling Innovation and Growth ”. At the same time, Student from various student branches displayed projects outside the auditorium and gave a brief description about the activities done by their Student Branches in Project Exhibit, SSC’18 where panel of judges came and judged student branches on various criteria.



Panel of judges at the stalls put up by student branches

The following are the winners of the exhibits - Vibrant Student Branch Award - Vaagdevi College of Engineering, Emerging Student Branch Award – Matrusri Engineering College and Vaagdevi Engineering College and finally the Lead Student Branch (Not in any order) went to three colleges - Jayamukhi Institute of Technology & Science, Gokaraju Rangaraju Institute of Engineering & Technology, Muffakham Jah College of Engineering & Technology.



Group photo of all participants at SSC 2018

In the Valedictory Ceremony, Awards were announced and , vote of thanks was presented by Dr. Y. Vijayalatha, SAC, IEEE Hyderabad Section.

IEEE Circuits and Systems/Electron Devices Chapter Activities (CAS/EDS)

One Week Faculty Development Program (FDP) on Latest Trends in VLSI Design and hands-on implementation using Cadence EDA Tools

IEEE CAS/EDS Chapter in association with Geetanjali College of Engineering, Hyderabad organized One Week Faculty Development Program (FDP) on “Latest Trends in VLSI Design and hands-on implementation using Cadence EDA Tools” on June 18-23 2018 at Geetanjali College of Engineering, Hyderabad.

The inauguration of the FDP was done in the august presence of Chief Guest Sri Anil Terkar, Scientist “G”, Head, Indigenous Processor Wing, ANURAG (DRDO), Guest of Honour Sri. A.G. Krishnakanth, Vice Chair, IEEE CAS/EDS, Senior Manager, AMS along with Dr. Udaya Kumar Susarla, Principal, GCET and Prof. B Hari Kumar, Head, Department of ECE along with Mr. S Vasu Krishna, Co-Coordinator of FDP, Associate Professor of ECE and IEEE SB Counselor, GCET. All the participants of the FDP were taken for a one day industrial visit to Gallium Arsenide Enabling Technology Centre (GAETEC), an MMIC Chip Fabrication Unit located in Hyderabad. A total of 28 members got registered in this FDP from various colleges across India, in which three are IEEE members. Certificates were distributed to all participants during valedictory session. Feedback given by the participants were very positive and requested to organize more events on Cadence Tools.



Inaugural session of the Faculty Development Program
Vasu Krishna, Hari Kumar, Anil Terkar, A.G. Krishnakanth, Udaya Kumar(L to R)



Certificate Distribution during Valedictory session by Arif Sohel and G. Venkat Reddy

Technical Talk on “High Level Synthesis of FPGA”

The IEEE CAS/EDS Chapter organised a technical talk on High Level Synthesis of FPGA by Prof. Mohammed A.S. Khalid, University of Windsor, Canada on August 20, 2018 at Seminar Hall of ECE Department, Osmania University. This event was facilitated by Dr. Rajendra Naik, Professor, ECED, OU and was conducted in collaboration with the OU ECE Alumni Association as Dr. Khalid is an alumni of ECE Department of OU. Dr. Khalid spoke on powerful high level synthesis (HLS) CAD tools such as Intel FPGA SDK for OpenCL and Xilinx Vivado which allow developers to utilize FPGA’s potential without long development time and extensive hardware design knowledge. It also enables rapid Design Space Exploration (DSE) for specific applications and will facilitate widespread adoption of FPGA technology since expertise in hardware design, HDLs and FPGAs is not required. The talk was attended by 42 persons of which about 8 were IEEE members. Dr. Khalid dedicated his talk to Late Dr. Mannan, former Head of MED, OU and Late Dr. D.V.R. Vitthal, former Head, ECED, OU who were his mentors at Osmania University during his student days.

Dr. Khalid also discussed about the research collaboration opportunities available with his university.



A section of the audience during the technical Talk



A memento being presented to Dr. Khalid

A Two Day Industry workshop on “Analog IC Design – Concept to Reality”

A two day Industry workshop on “Analog IC Design – Concept to Reality” was organised by IEEE CAS/EDS Chapter in collaboration with ams semiconductors India Pvt. Ltd at seminar hall of ams in iLABS, Hitech City. Over 60 persons had applied to attend this workshop out of which 40 participants were shortlisted, out of which about 18 were IEEE members. The workshop was conducted on 31 August and 1 September 2018. All faculty of the workshop were analog design engineers with over 15 years of experience in the field of analog IC Design and the workshop curriculum was designed to showcase the complete methodology that is followed in Analog IC

Design right from the concept stage till it is designed, fabricated, characterized and implemented in a system.



Vemuri H. Sai, Director, ams semiconductor

The workshop started with a welcome address by Mr. Hanuma Sai, Director, ams semiconductor India, who mentioned that Analog IC Design is a relatively difficult field of IC engineering when compared to digital IC design and there is a dearth of good analog designers in India. With this motive, ams is dedicated to conduct such workshops for faculty, students and young researchers who will in turn pass the benefit to a larger community, thus building an ecosystem for analog design. He also mentioned that ams works closely with the academia by supporting internships for M.E / Ph.D students and currently students from IIT Hyderabad, IIIT Hyderabad, Amrita University, University of Hyderabad, MJCET and other colleges are working in ams.



Participants of analog IC design workshop

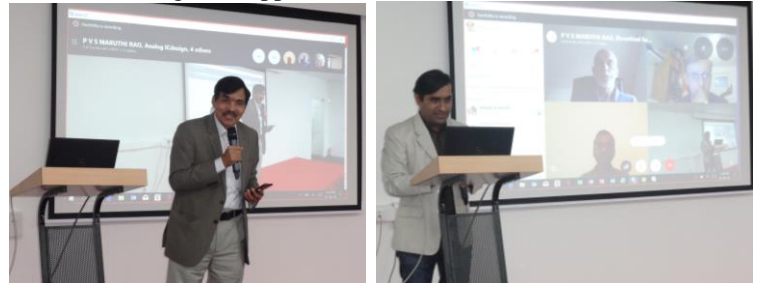
Dr. Arif Sohel, Chair of IEEE CAS/EDS in his address mentioned that Industry Institute interaction is a term that is regularly used in various forums but it is at ams semiconductors that one can see the true industry institute interaction happening. He also briefed the participants that this two day workshop will help them understand how analog IC design is done by practicing engineers but to excel in this field tool training is also mandatory. In this direction, the IEEE CAS/EDS Chapter is planning to organise a one week analog IC design camp on Cadence Tools, shortly.

The workshop started by a session on Motivation, Economics of IC Design, Understanding system/application requirements, Research opportunities in Circuits and Systems by Mr. A.G. Krishnakanth. This was followed by a detailed discussion on IC Architecture: Concept and Planning - Power partitioning, Analog/Digital Interface, Design Architecture, Test concept, Design for Specification, Pin-out, Package by Mr. Veeresh Babu. The final session was on Design for Test, Yield, Reliability/Quality, Robustness (ESD/EMI) by Mr. Sudhakar.

On the second day, a Practical Circuit design issues in Cadence Environment (Demo Session) was conducted by Mr. Sudhakar. In the afternoon session a talk on Layout and beyond (GDS to IC), Demonstrations by Ms. Harshita Penmetsa which also included a topic on Validation ATE testing, Qualification and “Unlimited” production. The event concluded with feedback from participants and certificate distribution by Prof. P.A. Govindacharyulu, Past Chair, IEEE CAS/EDS Chapter

Release of Call for Papers of MOS – AK India 2018

The IEEE CAS/EDS Society released the Call for Papers of IEEE International Conference on Modeling of Systems Circuits and Devices (Acronym MOS AK India 2019) on 1 September 2019 at ams semiconductor India Pvt. Ltd. The conference will be held from 25-27 February 2019 at IIT Hyderabad. The event started with Welcome address by Dr. Arif Sohel, Finance chair of the conference which was followed by address by Mr. A.G. Krishnakanth, Organising Chair of the conference who spoke about the conceptualization of MOS AK India 2019 and its return to India after 7 years. Dr. Wladek Grabinski, General Co Chair of the conference was present through SKYPE and spoke about the global efforts put by his organisation to promote Compact modeling. Dr. Ehrenfried Seebacher, ams, Austria and member steering committee discussed about the Industry ecosystem in device modeling and support of ams(A) to this conference.



Dr. M. Lakshminarayana and Mr. A.G. Krishnakanth addressing (also seen are Dr. Wladek Grabinski, Dr. Ehrenfried Seebacher, Dr. Anurag Mangla and Dr. Asudeb Dutta who joined through Skype Mode

Mr. V. Hanuma Sai, member, Steering committee highlighted on the Indian Industry scenario in this area. Prof. P.A. Govindacharyulu, General Co Chair, discussed about the academic research being undertaken across India in compact modeling. Dr. Asudeb Dutta, Publication Co Chair highlighted about his research activities in IIT Hyderabad. Dr. Kaleem Fatima, Tutorial chair introduced all the organising committee members and thanked them for their efforts. Dr. Merugu Lakshminarayana, Immediate past chair of IEEE Hyderabad section and member of steering committee discussed about the importance of organising good quality conferences and reiterated the support of IEEE Hyderabad section to MOS AK India 2019. Dr. Anurag Mangla, ams(A), Publicity Co chair and Dr. P. Chandrasekhar, OU, Publicity Co Chair were also present in the launch.



(L to R) Dr. Kaleem Fatima, Dr. Merugu Lakshminarayana, Dr. P.A. Govindacharyulu, Dr. Arif Sohel, Mr. V. Hanuma Sai, Mr. A.G. KrishnaKanth and Dr. P. Chandrasekhar launching the poster of Call for Papers of MOS AK India 2019

IEEE Young Professional (YP) Affinity Group Activities

Data Science Workshop at Sree Vidyanikethan Engineering College

IEEE Young Professionals, Hyderabad Section in joint collaboration with Sree Vidyanikethan Engineering College organized a Workshop for Young Faculty on Data Science during 19th – 23rd June 2018.

The workshop was organized with an objective to introduce data science to young faculty. Fruitful deliberations, and challenges in the field of data science were addressed, both theoretically and practically. The resource persons handled sessions on topics ranging from 'Machine Learning Classification' to 'Big Data Analytics' to Neural Networks'. The interactive and hands-on sessions were the highlights of the workshop.

All the participants expressed that the training in new foundational science, theoretical results, methods, applications and tools of data science was beneficial. Around 60 members participated in the workshop. The workshop came to a successful end with distribution of certificates, and feedback from participants in the valedictory session.



Group photo with participants

YP Meetup at Hotel Palm Beach Vishakhapatnam

IEEE Young Professionals, Hyderabad Section in collaboration with **IEEE Vizag Bay Sub Section** is organized YP Meetup at Hotel Palm Beach Vishakhapatnam on 19th May 2018. It started with a presentation from Saurabh Jain YP Chair who explained what is YP all about, who all are YP members. Deepak Mathur, Lance Fung, Dr. Amit Kumar, Atul Negi and Vinit Kumar addressed the gathering about challenges and Opportunities with YP and also briefed on various activities of IEEE which may help to mould the careers of professionals. Also, there was discussion on how social media can help in grooming individual's careers. Young Professionals of IEEE Hyderabad Section actively participated and shared their views in this event.



Group photo with participants

Research Workshop 2018 at JNTUH College of Engineering, Hyderabad.

IEEE Young Professionals, Hyderabad Section in joint collaboration with Department of CSE, JNTUH College of Engineering, WIE & IEEE India Council organized Research Workshop 2018 at Seminar Hall, Department of CSE, JNTUH College of Engineering, Kukatpally, Hyderabad from 23rd -24th June, 2018.

In this connection present IEEE YP Chair Saurabh and the Immediate past Chair Vinit Kumar Gunjan coordinated the entire program and discussed about the opportunities one can avail at this research workshop. This event carried out for two days successfully with the lectures delivered by eminent resource persons.



Group photo with participants

IEEE Computer Society(CS) Chapter Activities

Blockchain Summit-Driven with Practical Approach

IEEE Computer Society Chapter, Hyderabad Section Collaborated with Computer Society of India Hyderabad Chapter to organize a unique event "Blockchain Summit - a Practical Driven Approach" on July 20-21, 2018 at Anurag Group of Institutions, Hyderabad with an aim to bring in the practitioner view on the business applications driving the transformational changes in enterprises. The event was organized under the Leadership of Bala Prasad Peddigari, Chairman, IEEE Computer Society Chapter and Vidyasagar T, Vice-chair, IEEE Computer Society Chapter. Over 120 participants attended the event.



Participants in the Blockchain Summit

MGPL Narayana, Vice President and Chief Scientist, TCS graced the occasion as a Chief Guest to enlighten some of the important opportunities unfolding in the space of Blockchain. Mr. K Mohan Raidu, Chairman, Technology Council, CSI shared his views on Blockchain Transformation in IT and Mr. PVS Maruthi Rao, Chairman, IEEE Hyderabad Section shared his perspectives and impact of Blockchain in boutique companies. Mr. Madhav Negi,

Senior Member IEEE enlightened delegates on IEEE Membership and its benefits and invited delegates to become IEEE members.



P.V.S. Maruthi Rao, MGPL Narayana with other dignitaries

Membership development session by Madhav Negi

Various expert talks by eminent speakers like Ramakrishna Josyula and Sudhakar Kakkireni on Blockchain Use cases and real-time impact were delivered. Viswanadh Akella from SimplyFI presented a talk on Blockchain in Supply Chain Finance in a very engaging manner. NJ Rajaram touched upon the Reliability aspects of Blockchain, Sugandh Rakha from ServiceNow spoke on Blockchain for building products and determining important characteristics one need to consider. Later part of the evening was all about How Initial Coin Offering helping Blockchain Startups to raise investments by Raghu Kumar Uppada from Aircave Technologies and Sunil Kumar Bandireddi shared his thoughts on Blockchain and implications in Agriculture.

celebrations. There were expert lectures and visits to solar & wind power plants organized by the Subsection to bring awareness on the theme of PES Day. There were a total of 50 participants out of which 20 are IEEE members.



Dr.P.Lingareddy giving the expert lecture

The event started with a welcome note by Dr. S. V. N. Lalitha. She explained the importance of PES day celebrations. Dr. P. Lingareddy an eminent Professor of Electrical Engineering enlightened the students on the concept of renewable energy generation and the role of Green energy.

A group of students were taken to the renewable energy power plants. They visited a solar power plant and wind power plant. Students learnt practically how electrical energy is being generated in a solar power plant and wind power plant. They have understood the concepts of hybrid solar and wind power plant operation also. Students were given full exposure of how a hybrid solar wind power plant equipped with Batter energy storage system (BESS) operates and they really enjoyed it. The program was received well by all participants. The event concluded with an oath taken by the participants to spread the message of “More Power to the Future”.



Students at Wind power plant

Students exploring the BESS

The IEEE Day 2018 is being celebrated across the world on 2 October 2018. In this regard, all IEEE Chapters and student branches are requested to organise events to promote the cause of IEEE within a weeks time before or after 2 october. The same may be reported for section newsletter and also on the international website www.ieeeday.org



Organisers taking Oath for energy conservation

IEEE Guntur Sub Section Activities

Power & Energy Society(PES) Day

Power and Energy Society (PES) day celebrations were held in Guntur Subsection on 22nd April 2018 at various engineering colleges. Dr. S. V. N.L. Lalitha, Vice chair, Guntur Subsection and Professor, EEE Department, KLEF Deemed to be University has coordinated these

Call for articles / Individual member achievements

It is decided in the Section meeting that individual member accomplishments like publishing of a paper in IEEE journal/conference, taking up of a new position/responsibility (IEEE or any office of Public Interest), elevation to IEEE membership status, IEEE awards/Notable mentions can be mentioned in the newsletter under the achievements column to be started from next issue of newsletter. Please send in your articles/achievements for publication in newsletter to

synapse@ieeeday.org

Steering Committee

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PA Govindacharyulu, IEEE CAS/EDS, Hyderabad Section

Organizing Co-Chairs

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Tutorial Co-Chairs

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Ghanshyam Krishna, University of Hyderabad

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Gajendranath, IITH
M.A. Raheem, IEEE CAS/EDS, Hyderabad Section

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Larry Nagel, Omega Enterprises Consulting (USA)
Andrei Vladimirescu, UCB (USA); ISEP (FR)

MOS-AK Technical Committee

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Co-Chair: Prof. Carlos Galup-Montoro, UFSC; (BR)
Co-Chair: Sergio Bampi, UFRGS, (BR)
Co-Chair: Antonio Cerdeira Altuzarra, Cinvestav (MX)

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Co-Chair: Suba Subramaniam, XFab, (D)
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Modelling of Systems Working Group (MOS-AK)
Arbeitskreis Modellierung von Systemen und Parameterextraktion

2019 IEEE International Conference on
Modeling of Systems Circuits and Devices (MOS-AK India 2019)
Organised by Joint Chapter of CAS /ED Societies,
IEEE Hyderabad Section

Second MOS – AK India Conference (IEEE Conference #45395)
Venue: IIT Hyderabad Dates: February 25-27, 2019

Enabling International Compact Modeling R&D Exchange

Technical Program Promoters



Track 1: Compact / SPICE Modeling

Track 2: Integrated Circuits with emphasis on modeling

Call for Papers

MOS-AK is an international established HiTech forum to discuss the frontiers of electron device modeling with emphasis on simulation-aware compact/SPICE models. The main aim of the conference is to strengthen the network among experts in the field by serving as an open platform for information exchange related to compact/SPICE modeling and Verilog-A standardization, bringing people in the compact modeling field together, as well as obtaining feedback from technology developers, circuit designers, and CAD tool vendors. The topics cover all important aspects of compact model development, implementation, deployment and standardization within the main theme - frontiers of the compact modeling for nm-scale MEMS designs and CMOS/SOI circuit simulation.

The specific MOS-AK India 2019 R&D goal will be to classify the most important directions for the future development of the electron device models, not limiting the discussion to compact models, but including physical, analytical and numerical models, to clearly identify areas that need further research and possible contact points between the different modeling domains. This conference is designed for device process engineers (CMOS, SOI, BiCMOS, SiGe) who are interested in device modeling; ICs designers (RF/Analog/Mixed-Signal/SoC) and those starting in that area as well as device characterization, modeling and parameter extraction engineers. The content will be beneficial for anyone who needs to learn what is really behind IC simulation and modern device models.

Original unpublished works in topics related to the following areas (but not limited to) can be submitted for publication. The proceedings of the conference will be submitted to IEEE Explore. Best Paper Awards: Gold leaf, Silver leaf and Bronze leaf certificates will be given to best papers.

Compact Modeling Track

- Advances in semiconductor technologies and processing
- Compact Modelling (CM) of the electron devices
- Verilog-A language for CM standardization
- New CM techniques and extraction software
- FOSS TCAD/EDA modeling and simulation
- CM of passive, active, sensors and actuators
- Emerging Devices, TFT CMOS and SOI-based memory cells
- Organic, Bio/Med devices/technology modeling
- Microwave, RF, HV/Power device modeling
- Nanoscale CMOS devices and circuits
- Technology R&D reliability/aging, DFY, DFT and IC Designs
- Foundry/Fabless Interface Strategies

Circuits and Systems Track

- Analog Circuits
- Biomedical and Life-Science Circuits, Systems and Applications
- Circuits and Systems for Communication
- Emerging Technologies for Circuits and Systems
- HP/HV IC Designs
- Memory Circuits and Systems
- Mixed Signal Circuits
- RF/mm-Wave IC Design and Technology
- Sensory Systems System-on-Chip and CAD
- Testing Technology
- VLSI Systems & Applications
- And any other IC design related topic

Important Dates:

Call for Papers and Tutorials: 1 September 2018
Paper and Tutorial Submission Deadline: 1 November 2018
Notification of Acceptance: 15 December 2018
Registration and Camera Ready Paper submission: 10 January 2019
Release of Conference Program: 15 January 2018
Conference Dates: 25-27 February 2019

Conference Website: http://www.mos-ak.org/india_2019/

Paper Submission Link: <https://easychair.org/conferences/?conf=mosakindia2019>

Conference email: secretary.mosak.india@gmail.com



**IEEE Computational Intelligence Society Hyderabad Chapter Presents
6-Day Summer School 2018 on
Computational Intelligence: Theory, Implementation & Applications
22-27 November 2018**

Venue: Muffakham Jah College of Engineering & Technology (M.J.C.E.T.), Banjara Hills, Hyderabad, India.

Is located at Banjara Hills in the heart of the modern city of Hyderabad. M.J.C.E.T was established in 1980 by Sultan-Ul-Uloom Education Society. The college is affiliated to Osmania University, Hyderabad and approved by AICTE, New Delhi. MJCET figures in top 100 Engineering colleges in India. It is also identified as one of the most happening places in Hyderabad.

<https://goo.gl/maps/aM1LA5mzf8ro>

Program Chairs

- Dr. Atul Negi, Professor, School of Computer and Information Sciences, University of Hyderabad, Chairman IEEE Hyderabad Section (2012-13)
- Dr. Naresh M, Chairman, CIS/GRSS Joint Chapter, IEEE Hyderabad Section

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About IEEE Hyderabad Section
IEEE Hyderabad Section is recognized as an outstanding large section in IEEE Region 10 and has won several awards. The section has committed leadership and outstanding volunteers. Visit

About Summer School: IEEE is the world's largest professional organization dedicated to advancement of technology for the benefit of humanity. Here in this school from IEEE we bring a professional exposure to *Computational Intelligence (CI)*. CI is the evolution of AI technology and is adopted by industry giants for commercial use. IEEE Computational Intelligence Society with Geosciences & Remote Sensing Society are the chief technical sponsors of this program.

Many start-ups have spotted business opportunities powered by CI based solutions to customers. With the frontier of knowledge expanding fast globally the issue is that of manpower, training and awareness of the frontier.

IEEE Hyderabad CIS/GRSS Chapter hence is organizing this summer school (a tutorial program) with the support of IEEE Hyderabad Section. The present program comprises of tutorials and hands-on session by distinguished speakers both from industry and academia.

Program Details

The program is made into several thematic areas. The thematic areas are as given in the following.

- Theme 1: Artificial Neural Network & Applications**
- Theme 2: Evolutionary Computational Techniques**
- Theme 3: Fuzzy Logic Techniques**
- Theme 4: CI Applications**

Related hands-on sessions/demos are also part of the scheduled program.

Target Audience

- Scientists working in research organizations
- Faculty from Engineering/Science Institutions (AICTE etc.)
- Graduate students (M.Tech, MCA) Research scholars, (Ph.D, M.Phil) Post-doc
- IT Professionals interested in AI/ML
- Advanced Undergraduate students

International Registrations

- IEEE Student Members: USD 150
- IEEE Members USD 250
- Others: USD 400

Our Prominent Speakers

- Dr. P.N. Suganthan, NTU, Singapore
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Registration Details (in Rupees, INR)

Participants	IEEE	Non-IEEE
Student (PhD/PG/UG)	6500/- 4875/-	8000/- 6000/-
Academician	9500/- 7125/-	13000/- 9750/-
Industry	9500/- 7125/-	16000/- 12000/-

Note: Above fee structure is excluding boarding & lodging expenses.
SPONSORS

Important note

Early Bird Registration at 25% discount up to 15 Oct. 2018

- ✓ Registrations are limited, on first come, first served basis and last date for registration is **30 Sept. 2018** **05 Nov. 2018**
- ✓ Registration includes program kit, working lunch, high tea, and certificate. The payment should be made online by visiting the URL: the URL:
<https://www.payumoney.com/events/#/buyTickets/cis-ss2018>

Website: <https://cishyd-ss.ieeehyd.org>



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